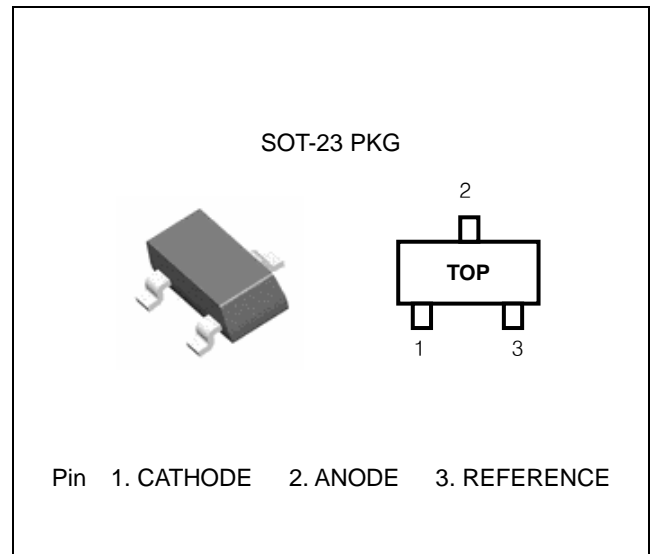


## FEATURES

- Programmable Output Voltage to 36V
- Extended Cathode Current Range 80 $\mu$ A to 100mA
- Low (Typ. 0.08 $\Omega$ ) Dynamic Output Impedance
- Adjustable Output Voltage
- Fast Turn-on Response
- Low Output Noise
- Excellent Temperature Coefficient 25ppm/ $^{\circ}$ C
- Available in SOT-23-3L Package

## APPLICATIONS

- Secondary Side Regulation in Flyback SMPS
- Industrial, Computing, Consumer and Portables
- Adjustable Voltage and Current Referencing
- Power Management
- Power Isolation
- Zener Replacement



## ORDERING INFORMATION

Device	Package
LPR431GxSF	SOT-23-3L

\* Refer to the ordering information for the details.

## DESCRIPTION

The LPR431 is a three-terminal adjustable shunt regulator with a specified thermal stability. The output voltage may be set to any value between  $V_{REF}$  and 36V with two external resistors. The active output circuitry provides a very sharp turn-on characteristic making these devices an excellent replacement for Zener diodes in many applications, such as on board regulation, adjustable power supplies, and switching power supplies.

## ABSOLUTE MAXIMUM RATINGS

(Full operating ambient temperature range applies unless otherwise noted.)

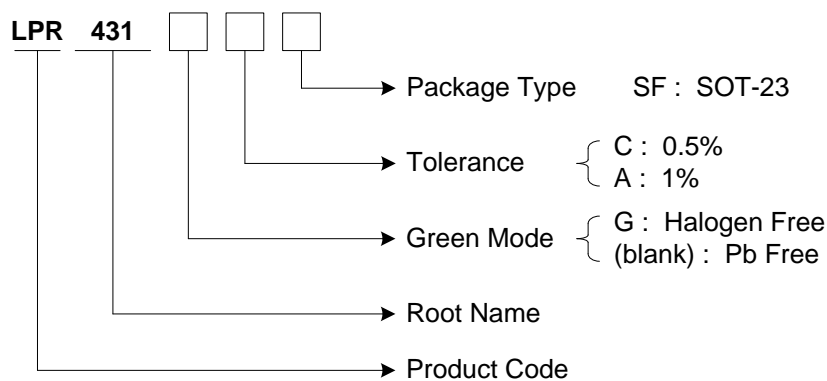
PARAMETER	SYMBOL	MIN.	MAX.	UNIT
Cathode Voltage	$V_{KA}$	-	40	V
Cathode Current Range	$I_{KA}$	-	150	mA
Reference Input Current Range	$I_{REF}$	-	10	mA
Junction Temperature Range	$T_J$	-40	150	$^{\circ}$ C
Operating Temperature Range	$T_{OPR}$	-40	125	$^{\circ}$ C
Storage Temperature Range	$T_{STG}$	-65	150	$^{\circ}$ C

## RECOMMENDED OPERATING CONDITIONS

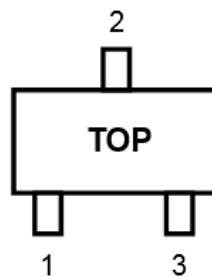
PARAMETER	SYMBOL	MIN.	MAX.	UNIT
Cathode Voltage	$V_{KA}$	$V_{REF}$	36	V
Cathode Current	$I_{KA}$	0.08	100	mA
Operating Temperature range	$T_A$	-40	85	°C

## ORDERING INFORMATION

VREF	Package	Tolerance	Order No.	Supplied As	Status
2.5 V	SOT-23-3L	0.5%	LPR431GCSF	Reel	Active
		1%	LPR431GASF	Reel	Active



## PIN CONFIGURATION

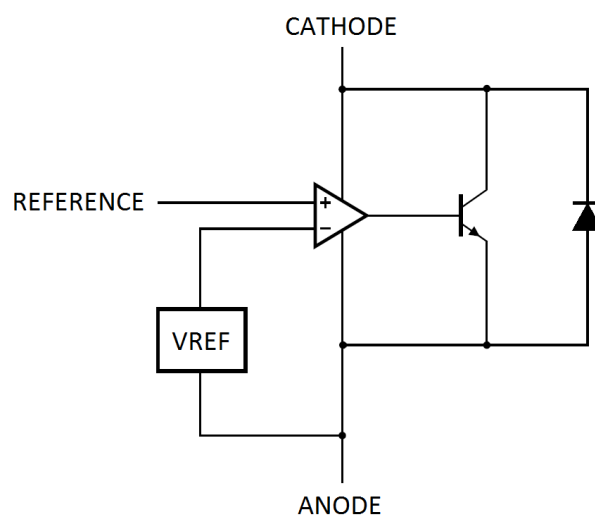


SOT-23-3L

## PIN DESCRIPTION

Pin No.	Pin Name	Pin Description
1	CATHODE	Input Supply Voltage
2	ANODE	Ground
3	REFERENCE	Reference Voltage

## BLOCK DIAGRAM



## ELECTRICAL CHARACTERISTICS

(T<sub>A</sub>=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Reference Input Voltage	V <sub>REF</sub>	V <sub>KA</sub> =V <sub>REF</sub> , I <sub>K</sub> =1mA	LPR431GC	2.487	2.500	2.512	V
			LPR431GA	2.475	2.500	2.525	
Deviation of Reference Input Voltage <sup>(Note 1)</sup>	ΔV <sub>REF</sub> /ΔT <sub>A</sub>	V <sub>KA</sub> =V <sub>REF</sub> , I <sub>K</sub> =1mA, T <sub>A</sub> =Full range	-	35	50	mV	
Ratio of Change in Reference Input Voltage to the Change in Cathode Voltage	ΔV <sub>REF</sub> /ΔV <sub>KA</sub>	I <sub>K</sub> =1mA	ΔV <sub>KA</sub> =10V to V <sub>REF</sub>	-	-1.0	-2.7	mV/V
			ΔV <sub>KA</sub> =36V to 10V	-	-0.4	-2.0	
Reference Input Current	I <sub>REF</sub>	I <sub>K</sub> =1mA, R1=10kΩ, R2=∞	-	180	500	nA	
Deviation of Reference Input Current <sup>(Note 1)</sup>	ΔI <sub>REF</sub> /ΔT <sub>A</sub>	I <sub>K</sub> =1mA, R1=10kΩ, R2=∞, T <sub>A</sub> =Full range	-	100	300	nA	
Minimum Cathode Current for Regulation	I <sub>K(MIN)</sub>	V <sub>KA</sub> = V <sub>REF</sub>	-	30	80	μA	
Off-State Cathode Current	I <sub>K(OFF)</sub>	V <sub>KA</sub> =36V, V <sub>REF</sub> =0	-	0.01	1	μA	
Dynamic Impedance <sup>(Note 2)</sup>	Z <sub>KA</sub>	V <sub>KA</sub> = V <sub>REF</sub> , I <sub>K</sub> =0.2mA~100mA, f ≤ 1kHz		0.08	0.3	Ω	

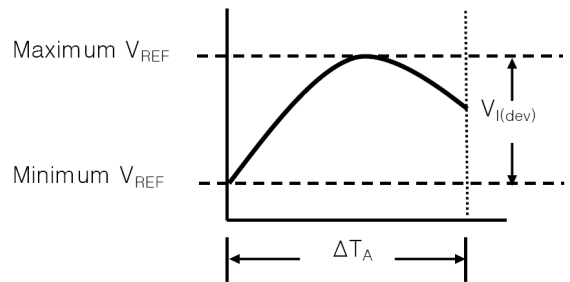
(Note 1) The deviation parameters ΔV<sub>REF</sub>/ΔT<sub>A</sub> and ΔI<sub>REF</sub>/ΔT<sub>A</sub> are defined as the differences between the maximum and minimum values obtained over the recommended temperature range. The average full-range temperature coefficient of the reference voltage, αV<sub>REF</sub>, is defined as:

$$|\alpha V_{REF}| \text{ (ppm/°C)} = \frac{\left( \frac{V_{I(\text{dev})}}{V_{REF} \text{ at } 25^\circ\text{C}} \right) \times 10^6}{\Delta T_A}$$

Where:

ΔT<sub>A</sub> is the recommended operating free-air temperature range of the device.

αV<sub>REF</sub> can be positive or negative, depending on whether minimum V<sub>REF</sub> or maximum V<sub>REF</sub>, respectively, occurs at the lower temperature.

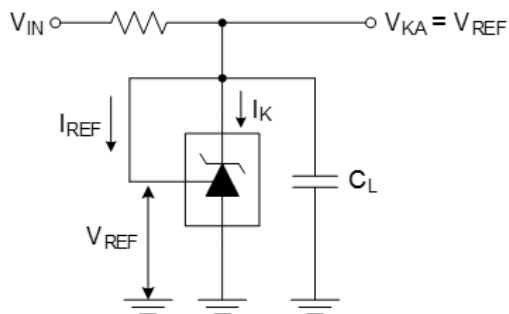


(Note 2) The dynamic impedance is defined as:  $|Z_{KA}| = \frac{\Delta V_{KA}}{\Delta I_{KA}}$

When the device is operating with two external resistors, the total dynamic impedance of the circuit is given by:

$$|Z| = \frac{\Delta V}{\Delta I} \approx |Z_{KA}| (1 + R1/R2)$$

## TEST CIRCUITS



< Fig 1. Test circuit for  $V_{KA} = V_{REF}$  >

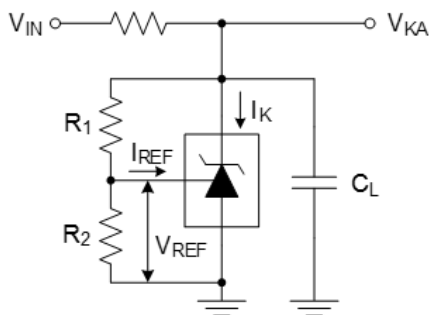
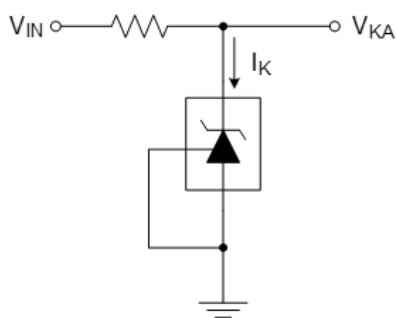


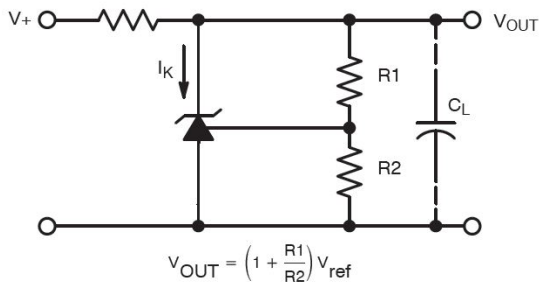
Fig 2. Test circuit for  $V_{KA} \geq V_{REF}$  >



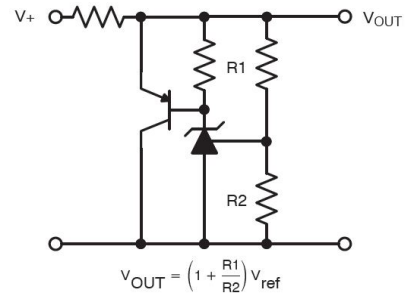
< Fig 3. Test circuit for  $I_{KA(OFF)}$  >

## TYPICAL APPLICATION CIRCUIT

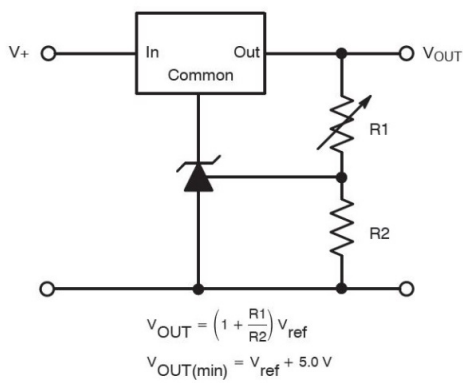
1. Shunt Regulator



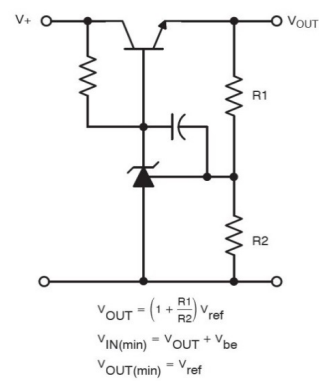
2. High Current Shunt Regulator



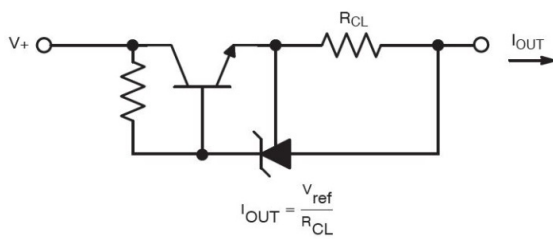
3. Output Control for a Three-Terminal Fixed Regulator



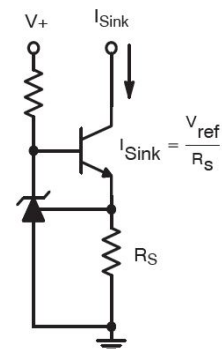
4. Series Pass Regulator



5. Constant Current Source



6. Constant Current Sink



## REVISION NOTICE

The description in this datasheet is subject to change without any notice to describe its electrical characteristics properly.